Task No 01:

Implement half adder and full adder circuit using XOR and AND gates on Multisim.

Solution:

Output:

Task No 02:

Implement half subtractor and full subtractor circuit using XOR gate on Multisim.

Solution:

Output:

Task No 03:

Implement half adder and full adder circuit using NAND gate on Multisim.

Solution:

Output:

Task No 04:

Implement 4-bit adder circuit on Multisim.

Solution:

Output:

Task No 05:

Implement 4-bit subtractor circuit on Multisim.

Solution:

Output: